

## Seminar on VLSI Design & Microelectronics Technology

On the 8th of September, 2018 the Department of Electronics and Communication Engineering in association with SPIE Student Chapter, BPPIMT organized a Seminar on VLSI Design & Microelectronics Technology. This Seminar on VLSI Design & Microelectronics Technology provided an open forum to faculty members, graduate and undergraduate students with an emphasis on Design of VLSI Systems and Microelectronics Technology.

112 members participated – faculties and students, in this Seminar. We invited 6 students from outside colleges as well making this seminar's enlightening boundary even wider.

Eminent speakers from all over Kolkata came to enlighten young minds and provide valuable knowledge and information on the budding topic of VLSI Design and Microelectronics Technology.

It was our pleasure to welcome to the campus eminent speakers- Prof. Susmita Sur Kolay (ISI, Kolkata), Prof. Susanta Sen (Institute of Radio Physics and Electronics, University of Calcutta), Prof. Chandan Kumar Sarkar (Jadavpur University) and Prof. Tarun Kanti Bhattacharyya (IIT, Kharagpur) to share their knowledge and experiences in this field and make this seminar a huge success.

