Dr. Bhaskar Som

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Educational Qualification:

Degree	***Year***	***Board/University***	***Institute***
Ph.D (IMPATT diode)	1977	Calcutta University	University College of Technology
M.Tech (Radio Physics & Electronics)	1968	Calcutta University	University College of Technology
B.Tech (Radio Physics & Electronics)	1967	Calcutta University	University College of Technology

Work Experience:

Type of Job	***Organization***	***Designation***	***Duration***
Academic	B. P. Poddar Institute of Management & Technology	Professor	11 years
Industrial	Philips India	Development Manager	32 years

Selected in IES in 1970.

Research Interests: Microwave

Teaching Interest: Electronics related subjects

List of Publications:

Reviewed the book “Microelectronics Circuit Design”, R.C.Jaeger & T.N.Blalock, Tata McGraw-Hill Companies in 2006.

Journal

1. “Small Signal Analysis of Harmonic Modes in An Avalanche Diode” Int.I.Electronics,Vol.28, pp 191-196, 1970.
2. “A Small Signal Analysis of the second Harmonic Modes in a Uniformly Avalanching p-i-n Diode having Unequal Ionization Rates and Drift Velocities for Electrons and Holes” -Int. I. Electronics, vol-30, pp 365-379, 1971
3) ”Analysis of Space Charge Induced Negative Resistance in Linearly Graded Avalanching Silicon p-n junctions”-Solid State Electronics, vol.15 pp 39-44, 1972
4) “Small Signal Analysis of IMPATT Device having two Avalanche Layers Interspaced by a Drift Layer” Solid State Electronics vol17, pp-1029-1038, 1974.
5) “Dependence of Read Diode Characteristics on the Current Multiplication Factor in the Avalanche Zone” -Proc. IEEE, vol.63, pp 1072-1073, 1975.
6) “Effect of Finite Current Multiplication Factor in the Avalanche Zone on the High Frequency Noise Properties of Read Diodes” - Proc. IEEE,Aug,1976