



Seminar on VLSI Design and Microelectronics Technology
8th September, 2018
 Jointly Organized by
Department of Electronics & Communication Engineering
 &
SPIE Student Chapter
B.P. Poddar Institute of Management & Technology



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The aim of this Seminar is to provide an open forum for faculty members, graduate and undergraduate students, engineers and scientists with an emphasis on Design and Testing of VLSI Systems and Microelectronics Technology. Eminent Speakers will enlighten the audience from various perspectives and thereby provide an overview on recent trends in this field. The Seminar will promote advance learning, discussions, and collaborative works along with development of innovative ideas.

Topics

- Analog and Digital System Design
- VLSI testing
- FPGA Based System Design
- Formal Verification Technique
- Embedded System & Applications
- Emerging Technology
- Microelectronics Devices

Invited Speakers (Tentative)

- Prof. Susanta Sen
 Institute of Radio Physics and
 Electronics, University of Calcutta
- Prof. Chandan Kr. Sarkar
 Jadavpur University, Kolkata
- Prof. Susmita Sur Kolay
 ISI, Kolkata
- Prof. Tarun Kanti Bhattacharyya
 IIT, Kharagpur

Registration Fees:

Industry Person: Rs. 500 /-
 Academician/ Scientists: Rs. 250/-
 Students/Researcher (Non SPIE):
 Rs. 150/-
 Students/Researcher (SPIE):
 Rs. 100/-

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Seminar on
VLSI Design and Microelectronics
Technology
08.09.18
 Organized by
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Registration Form

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