



NOTICE

Date: 29.08.2018

It is an immense pleasure to inform all that a seminar on 'VLSI Design & Microelectronics Technology' has been jointly organized by Department of Electronics & Communication Engineering and SPIE Student Chapter, BPPIMT, on 8th September, 2018 from 09:30 a.m. onwards at Room-604, B-block.

The seminar is scheduled as follows:

Date/Time: 8th September, 2018. 9.30 am – 5:00 pm
Venue: Room-604, B-Block, Department of Electronics & Communication Engineering, BPPIMT, Poddar Vihar, 137, VIP Road, Kolkata-52
Speakers: **Prof. Susanta Sen, University of Calcutta**
Prof. Chandan Kumar Sarkar, Jadavpur University
Prof. Susmita Sur Kolay, ISI, Kolkata
Prof. Tarun Kanti Bhattacharyya, IIT Kharagpur
Contacts: Ms Banhi Das-9903292565, Mr. Prasenjit Kr. Mudi-6290006234
Mr. Nilabhra Saha-9836702773, Shirsho Dutt-8013835243
Online Registration:
<https://docs.google.com/forms/d/1s6VXeggq4gsk4vh31niTEIulChOk95G88Kc32Xfs7uw/edit>

For detail: <http://bppimt.ac.in/>

Note: All the classes and sessional will commence as per schedule.

Dr. Ivy Majumdar
(HOD, ECE Dept.)

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